sensor means, which are exposed to the resulting magnetic field of the primary and secondary coils,

a booster circuit, which is down-streamed to the sensor means at the input, and which feeds the compensation current to the secondary winding via the terminating resistor at the output, whereby the compensation current is pulse-duration modulated with a timing frequency above the resonance frequency of the converter, and

a low-pass filter arrangement for stabilizing the pulse-duration modulated compensation current, which is down-streamed to the booster circuit, comprising inductances and capacitances, which possesses a filter frequency threshold below the resonance frequency of the converter, and below the timing frequency of the booster circuit, as well as excessive resonance, whereby the excessive resonance of the low-pass filter arrangement is damped by an RC element connected in parallel to the secondary winding and the terminating resistor.

2. (Amended) The current sensor in accordance with claim 1, wherein the secondary winding is divided into a multitude of secondary coils, whereby the excessive voltages occurring between the secondary coils is limited by limiting means.

3. (Amended) The current sensor in accordance with claim 2, wherein the limiting means are Zener diodes, which are connected in a series, polarized in reverse order, and which are connected in parallel to the secondary coils.

4. (Amended) The current sensor in accordance with claim 1, wherein the booster circuit has at least one reverse timing power amplifier.

5. (Amended) The current sensor in accordance with claim 4, wherein the terminating resistor and the secondary winding are connected via a bridge circuit between reverse timing power amplifiers.

Version with markings to show changes made:

In the Claims:

Kindly amend the claims as follows:

1. (Amended) A [C]current sensor working in accordance with the compensation principle [with] comprising:

a primary winding [(2)] through which the current to be measured flows, creating a magnetic field,

a secondary winding [(4)], through which compensation current flows, which generates a magnetic field compensating the primary winding, whereby the primary winding and the secondary winding combined form a converter with certain resonance frequencies,

a terminating resistor [(19)] connected in a series to the secondary winding,

sensor means [(7)], which are exposed to the resulting magnetic field of the primary and secondary coils,

a booster circuit [(8 through 12)], which is down-streamed to the [secondary winding] sensor means at the input, and which feeds the compensation current to the secondary winding [(4)] via the terminating resistor [(19)] at the output, whereby the compensation current is pulse-duration modulated with a timing frequency above the resonance frequency [(25)] of the converter, and

a low-pass filter arrangement [(17, 18)] for stabilizing the pulse-duration modulated compensation current, which is down-streamed to the booster circuit [(8 through 12)], [consists of] comprising inductances [(20, 21)] and capacitances [(22, 23)], which possesses a filter frequency threshold below the resonance frequency [(25)] of the converter, and below the timing frequency [(29)] of the booster circuit, as well as excessive resonance, whereby the excessive resonance

of the low-pass filter arrangement [(17, 18)] is damped by an RC element [(40)] connected in parallel to the secondary winding [(4)] and the terminating resistor [(19)].

2. (Amended) The [C]current sensor in accordance with [one of the] claim 1, [in that] wherein the secondary winding [(4)] is divided into a multitude of secondary coils [(5, 6)], whereby the excessive voltages occurring between the secondary coils [(5, 6)] is limited by limiting means [(44, 45)].

3. (Amended) The [C]current sensor in accordance with claim 2, [in that] wherein the limiting means are Zener diodes [(44)], which are connected in a series, polarized in reverse order, and which are connected in parallel to the secondary coils [(5, 6)].

4. (Amended) The [C]current sensor in accordance with [one of the] claim[s] 1 [through 3], [in that] wherein the booster circuit has at least one reverse timing power amplifier [(11, 12)].

5. (Amended) The [C]current sensor in accordance with claim 4, [in that] wherein the terminating resistor [(19)] and the secondary winding [(4)] are connected via a bridge circuit between [both] reverse timing power amplifiers [(11, 12)].